

ATLANTA -- Scientists from around the world will have new opportunities to study the stars thanks to a newly installed mobile telescope at Georgia State's Center for High Angular Resolution Astronomy, known as the CHARA Array. The new -- and seventh -- telescope is mounted on a trailer that can be moved between locations near the other ...

In this course, learners will receive an overview of phased array radar system requirements and operation, which are reinforced by application examples. Phased Array Radar Systems - System Engineering | Georgia Tech Professional Education

The Adaptive Arrays: Advanced Topics and Applications course presents advanced topics and applications in adaptive signal processing algorithms. This one and a half day course is intended to be a follow-on to Adaptive Arrays: Fundamentals, but can also be offered as a stand-alone course. The first days starts with a review of space-time detection theory and then develops ...

The Adaptive Arrays: Fundamentals course presents fundamental concepts in adaptive signal processing algorithms and digital beamforming architectures that can be applied to any type of sensor array, with emphasis on radar and communications applications. This two-day course starts with a focused review of radar and phased array architectures. Then the course ...

Once used only for military applications, phased array antennas are now being employed in industries as varied as healthcare, automotive and communications. With our Phased Array Antenna Engineering Certificate, you'll learn how the different types of phased arrays compare and how to determine which is best for your needs.

These eNVMs are mostly pursued as the next-generation storage-class memory or embedded memory technologies with aggressive industrial research and development, and we have extensive research activities in HfO₂ based RRAM and ferroelectric devices. We are interested in the following topics: 1.

As a key feature of the project, GTRI researchers collaborated with academic researchers in Georgia Tech's School of Electrical and Computer Engineering (ECE) to utilize SoloPulse, a new array processing algorithm ...

Modeling and Simulation of Phased-Array Antennas Section Details Course Code DEF Course ID 4015P Term 202500 CRN 25015 Section ... Claim your Georgia Tech (GT) account, set up your GT account password and view your GT account user name. This account is different from your account and password on this website.

Using microfluidic passages cut directly into the backsides of production field-programmable gate array (FPGA) devices, Georgia Institute of Technology researchers are putting liquid cooling right where it's needed the ...

Researchers from Georgia Tech's College of Engineering have developed a novel and flexible solution to address the problem. Their additively manufactured tile-based approach can construct on-demand, massively ...

Abhijit Chatterjee is a professor in the School of Electrical and Computer Engineering at Georgia Tech and a Fellow of the IEEE. He received his Ph.D in electrical and computer engineering from the University of Illinois at Urbana-Champaign in 1990. Dr. Chatterjee received the NSF Research Initiation Award in 1993 and the NSF CAREER Award in 1995.

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In this course, you will receive an overview of phased array radar system requirements and operation, which are reinforced by application examples. You'll have the opportunity to examine major subsystems and associated technologies with specialists in those areas. There will be a focus on phased array antenna principles and design, as well as on methodologies for search ...

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A ARRAY Technologies é líder global na transformação do futuro da energia limpa. Com mais de 30 anos de inovações que impulsionaram a indústria solar, a ARRAY está posicionada de forma única para fornecer soluções de energia renovável para clientes que buscam adotar energia limpa em mercados ao redor do mundo. Embora nossa força ...

You will learn about the basics of array theory, simulation approaches, the impact of real-world errors and methods for array calibration, system-level impacts including bandwidth and subarraying, component technologies and their functions and uses, emerging digital techniques and their benefits, and the implementation of digital hardware.

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